

**Patent** 

ormation Disclosure Statement Transmittal

						7	<i>'</i> ~	
Thereby ce	ertify that this tran	ismittal of the below desc	cribed document is being	deposited with the United State	s Postal Ser	vice in a	n envelop	é bearing First
Class Post	age and addresse	ed to the Commissioner of	of Patents and Trademar	ks, P.O. Box 1450, Alexandria, V	/A 223/3/14	50, on ti	ne below d	late of deposit.
Date of Deposit:	09/14/2004	Name of Person Making the Deposit:	David Castro	Signature of the Person Making the Deposit:	W	U an	[.]:	)2C ]

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: M. Thompson, et al.

Examiner:

Serial No.:

10/758,146

Confirmation No.: 4640

Filed:

01/14/2004

Art Unit: -

2825

For:

Method and System for Integrated Circuit Backside Navigation

The Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, VA 22313-1450

OII.	
	Information Disclosure Statement Transmittal
Transmitted herewith is the follow	ing:
Formal drawings, totaling	sheets.
Informal drawings, totaling	sheets.
Certification for PTO Conside	eration
X Information Disclosure stater	ment, totaling 1 sheet.
Information Disclosure staten	nent and late filing fee
X Form 1449	
Petition for Extension of Tim	e
V Other: 2 Deferences	

Fee Calculation (for other than a small entity)		
Fee Items	Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere	\$ .00	\$ .00
Information Disclosure Statement, late filing	\$180.00	\$ .00
Other:		\$ .00
Total Fees		\$ .00

#### **PAYMENT OF FEES**

- The full fee due in connection with this communication is 1. provided as follows:
- The Commissioner is hereby authorized to charge any additional fees associated with this [ X ] communication or credit any overpayment to Deposit Account No.: 23-0085. A <u>duplicate copy</u> of this authorization is enclosed.
- A check in the amount of \$ 0.00
- [ X ] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060 direct (408) 938-9069 facsimile Customer No.: 41066

Respectfully submitted,

Date: September 14, 2004

Eric J. Gash

Reg. No.: 46,274



### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of: M. Thompson, et al.

Examiner:

Serial No.:

10/758,146

Confirmation No.: 4640

Filed:

01/14/2004

Art Unit:

2825

For:

Method and System for Integrated Circuit Backside Navigation

Commissioner of Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

Pat. No.	Pat. Title	Grant Date
US 5,948,217	METHOD AND APPARATUS FOR ENDPOINTING WHILE MILLING AN INTEGRATED CIRCUIT	09/07/1999
US 6,518,571	THROUGH-THE-SUBSTRATE INVESTIGATION OF FLIP-CHIP IC'S	02/11/2003

Please direct all correspondence concerning the above-identified application to the following address:

### WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060 Customer Number: 000041066

Respectfully submitted,

Date: September 14, 2004

Reg. No. 46,274

1 of 1



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## **Patent Application**

In re Application of: M. Thompson, et al.

Examiner:

Serial No.:

10/758,146

Confirmation No.: 4640

Filed:

01/14/2004

Art Unit:

2825

For:

Method and System for Integrated Circuit Backside Navigation

# Form 1449

## **U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	Α	US 5,948,217	09/07/1999	Winer, et al.	204	192.34	12/20/1996
	В	US 6,518,571	02/11/2003	Talbot, et al.	250	307	02/10/2001
	С						

Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Translation	
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	E							
	F							
	G							
	Ξ							
	_							
	J							

### **Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	K	
	L	
	М	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.